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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/726,802	12/02/2003	Osamu Kobayashi	GENSP014	4125	
22434	7590 02/10/2006		EXAMINER		
BEYER WEAVER & THOMAS LLP			LEE, CHUN KUAN		
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			2181	2181	
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Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
Office Action Summary		10/726,802	KOBAYASHI, OSAMU			
		Examiner	Art Unit			
		Chun-Kuan (Mike) Lee	2181			
	The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply					
A SHOWHIC - Externatter - If NO - Failu Any r	ORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DATES and it is a solid so	ATE OF THIS COMMUNICATION 16(a). In no event, however, may a reply be time rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONEI	I. lely filed the mailing date of this communication. O (35 U.S.C. § 133).			
Status						
2a)⊠	Responsive to communication(s) filed on <u>21 Not</u> This action is FINAL . 2b) This Since this application is in condition for allowant closed in accordance with the practice under <i>E</i>	action is non-final. nce except for formal matters, pro				
Dispositi	on of Claims					
5) [Claim(s) 1-20 is/are pending in the application. 4a) Of the above claim(s) is/are withdraw Claim(s) is/are allowed. Claim(s) 1-20 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/or on Papers	vn from consideration.				
10)⊠	The specification is objected to by the Examiner The drawing(s) filed on <u>02 December 2003</u> is/ar Applicant may not request that any objection to the or Replacement drawing sheet(s) including the correction The oath or declaration is objected to by the Example 1.	re: a) \square accepted or b) \square objected are discovered. See done is required if the drawing(s) is object.	e 37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).			
Priority u	ınder 35 U.S.C. § 119					
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
Attachmen	t(s) e of References Cited (PTO-892)	4) Interview Summary	(PTO-413)			
2) Notic 3) Inform	e of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date 12/05/2005.	Paper No(s)/Mail Da				

Response to Arguments

1. Applicant's arguments filed 11/21/2005 have been fully considered but they are

not persuasive.

2. Amendment to the claim 15 is acknowledged and 35 U.S.C. § 101 rejection to

claims 15-20 are withdrawn.

3. In respond to applicant's argument regarding claims 1, 8 and 15 rejections under

35 U.S.C. § 103(a) that the reference Kim (US Patent 6,577,303) cannot be combined

with Clark (US Patent 5,949,437) because Kim propose that the display can only be a

digital type display and Clark's teaching propose that the display device can be either a

digital or an analog display device without restriction and furthermore, Kim ('303) does

not provide for configuration of the connector to accommodate the situation where either

one or both of the video source or video display is analog in nature. The argument has

been fully considered but is not found to be persuasive because of the following

reasons:

Examiner does not agree, Kim clearly include the teaching wherein the display

device can also be an analog device, such as an analog video display device having a

cathode ray tube (CRT), wherein the DVI-I provides the appropriate interconnection for

analog signals (col. 1, II. 17-50), therefore, in utilizing the DVI-I connection, the

configuration of the connector must accommodate the situation where either one or both

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. . .

of the video source or video display is analog in nature. While applicant agrees that DVI-I and DVI-D use two distinct connections, it should be emphasized that DVI-I supports both analog and digital, therefore, clearly DVI-I is directly applicable.

4. In response to applicant's argument that the reference Kim ('303) does not teach configuring the connector based upon a determination of the analog or digital nature of both the video source and the video sink connected thereby. The argument has been fully considered but is not found to be persuasive because of the following reasons:

Examiner is not relying on Kim's ('303) teaching along. Examiner is relying on the teaching of both Kim ('303) and Clark ('473), please view the rejection to claim 1 as discussed below for further details.

5. Therefore, all dependent claims depending either directly or indirectly on claims 1, 8 and 15 are unpantentable, at least because they include all the limitations recited in claims 1, 8 and 15. Examiner reiterates the rejections for claims 1-20 in detail below.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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6. Claims 1, 8 and 15 are rejected under 35 U.S.C. 103(a) as being unpatentable over <u>Kim</u> (US Patent: 6,577,303) in view of <u>Clark</u> (US Patent: 5,949,437).

7. As per claims 1, 8 and 15, <u>Kim</u> teaches a system method and computer program product executable by a processor having connecting a video source and a video display, comprising:

coupling a video signal to a digital video display device with a coupling device (Figure 1 and column 4, lines 7-34, where "video signal" is read on "video source" and "digital video display device" is read on "video display");

automatically determining whether the video signal is an analog video signal or a digital video signal (column 4, lines 35-43);

configuring the coupling device base on the determination stated above (Figure 2 and column 4, lines 35-43);

a controller for making an automatic determination of whether the video signal is the analog video signal or the digital video signal (<u>Kim</u>, Figure 1 and column 4, lines 44-60, where "controller" is read on "processor"); and

a video signal switch for configuring the coupling device under control of the controller according the automatic determination (<u>Kim</u>, Figure 1 and column 4, lines 29-65, where "video signal switch" is read on "switch").

<u>Kim</u> fails to teach automatic determination whether the video display is an analog video display or a digital display.

Clark teaches a system method and computer program product having connecting a video source and a video display, comprising automatic determining whether the monitor is analog or digital (Figure 6 and column 5, line 49 to column 6 line 14, where "monitor" is read on "video display", "analog" is read on "analog video display" and "digital" is read on "digital display").

Therefore, it would have been obvious to one of ordinary skill in this art, at the time of invention was made to have modified <u>Kim</u> to include the automatic determination whether the monitor is analog or digital.

It would have been obvious to one of ordinary skill in this art, at the time of invention was made to have modified <u>Kim</u> by the teaching of <u>Clark</u>, because the automatic determination whether the monitor is analog or digital would reduce manufacturing cost by reducing hardware requirements, such as utilizing a single buffer to support multiple displays (<u>Clark</u>, Abstract).

- 8. Claims 2-4, 9-11 and 16-18 are rejected under 35 U.S.C. 103(a) as being unpatentable over <u>Kim</u> (US Patent: 6,577,303) and <u>Clark</u> (US Patent: 5,949,437) as applied to claims 1, 8 and 15 above, and further in view of the "<u>Digital Visual Interface</u> (DVI), Revision 1.0".
- 9. As per claims 2, <u>Kim</u> and <u>Clark</u> teach that the system method and computer program product having connecting the video source and the video display conforms to the Digital Visual Interface (DVI) standard (Kim, column 4, lines 1-12).

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Kim and Clark do not to teach configuration of the coupling device as a doubly terminated twisted pair type connector having a number of communication channels included therein.

The "<u>Digital Visual Interface (DVI)</u>, <u>Revision 1.0</u>" teaches the use of a T.M.D.S. (transition minimized differential signaling) differential pair connector to interconnect the transmitter and receiver (Figure 4-1 and section 4.1 on page 33, where "T.M.D.S. differential pair" is read on "doubly terminated twisted pair type") having a number of communication channels included therein (Figure 2-1, page 10) for both digital and analog video signal and both digital and analog video display.

Therefore, it would have been obvious to one of ordinary skill in this art, at the time of invention was made to modified <u>Kim</u> and <u>Clark</u> to include the configuration of the coupling device as the T.M.D.S. differential pair connector having a number of communication channels included therein for communication between the digital video signal and the digital video display.

It would have been obvious to one of ordinary skill in this art, at the time of invention was made to have modified <u>Kim</u> and <u>Clark</u> by the teaching of the "<u>Digital Visual Interface (DVI)</u>, <u>Revision 1.0</u>", because <u>Kim</u> stated that the video display connection system method conforms to the DVI standard, therefore enabling to communicate video signals over a T.M.D.S. differential pair connection, therefore allowing communication of video data over the differential pair connector having a number of communication channels.

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10. As per claim 3, Kim, Clark and "Digital Visual Interface (DVI), Revision 1.0" teach that the system method and computer program product having connecting the video source and the video display further comprising that the DVI standard supports the Extended Display Identification Data (EDID) specification, wherein both DVI compliant systems and monitors must support the EDID data structure ("Digital Visual Interface (DVI), Revision 1.0", section 1.3.2 on page 8, where "data structure" is read on "packetizing and depacktizing of data"); and therefore the video display connection system method further comprises:

receiving data from the graphic controller (Figure 2-1 in page 10, where "graphic controller" is read on "video source");

packetizing the video data to form a packetized video data stream formed of a number of video data packets;

passing the video data packets by way of selected ones of the communication channels from the video source to the monitor;

depacketizing the video data packets at the T.M.D.S. receiver; and generating a displayable image based upon the depacketized video data (Figure 2-1 in page 10, where "T.M.D.S. receiver" is read on "video display").

11. As per claim 4, <u>Kim</u>, <u>Clark</u> and "<u>Digital Visual Interface (DVI)</u>, <u>Revision 1.0</u>" teach that the system method and computer program product having connecting the video source and the video display further comprising:

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encoding video data from the video source from an 8-bit format to a 10-bit format ("<u>Digital Visual Interface (DVI)</u>, Revision 1.0", Figure 2-1 and section 2.1 on page 10 and section 3.1.4 on page 25);

transmitting the encoded video data from the T.M.D.S. transmitter to T.M.D.S. receiver ("<u>Digital Visual Interface (DVI)</u>, Revision 1.0", Figure 2-1 and section 2.1 on page 10, where "T.M.D.S. transmitter" is read on "video source" and "T.M.D.S. receiver" is read on "video display");

decoding video data from the 10-bit format to the 8-bit format at the T.M.D.S. receiver ("<u>Digital Visual Interface (DVI)</u>, <u>Revision 1.0</u>", Figure 3-6 and section 3.3 on pages 30-31, where "decoding" is read on "converting the encoded"); and

providing the data to the display control in the 8-bit format ("<u>Digital Visual Interface (DVI)</u>, Revision 1.0", Figure 2-1 and section 2.1 on page 10, where "display control" is read on "video display").

- 12. Claims 9-11 and 16-18 repeat the limitations of claims 2-4 and are therefore rejected accordingly.
- 13. Claims 5-6, 12-13 and 19-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over <u>Kim</u> (US Patent: 6,577,303), <u>Clark</u> (US Patent: 5,949,437) and the "<u>Digital Visual Interface (DVI)</u>, <u>Revision 1.0</u>" as applied to claims 4, 11 and 18 above, and further in view of <u>Bauch et al.</u> (US Pub.: US 2003/0152160).

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14. As per claim 5, <u>Kim</u>, <u>Clark</u> and "<u>Digital Visual Interface (DVI)</u>, <u>Revision 1.0</u>" teach that the system method and computer program product having connecting the video source and the video display further comprising two T.M.D.S. links ("<u>Digital Visual Interface (DVI)</u>, <u>Revision 1.0</u>", Figure 2-1 and section 2.1 on page 10).

Kim, Clark and "Digital Visual Interface (DVI), Revision 1.0" do not teach that the system method and computer program product having connecting the video source and the video display comprising wherein the communication channel is formed of a main link having an associated main link data rate and an auxiliary link having an auxiliary link data rate

Bauch teaches a video display connection system method comprise wherein the communication channel is formed of a primary link having an associated pri_clk and a secondary link having a sec_clk (Figure 3 and [0033] in page 3, where "primary link" is read on "main link", "pri_clk" is read on "main link data rate", "secondary link" is read on "auxiliary link" and "sec_clk" is read on "auxiliary link data rate").

Therefore, it would have been obvious to one of ordinary skill in this art, at the time of invention was made to modified <u>Kim</u>, <u>Clark</u> and "<u>Digital Visual Interface (DVI)</u>, <u>Revision 1.0</u>" to include the formation of communication by the primary link having the associated pri_clk and the secondary link having the sec_clk.

It would have been obvious to one of ordinary skill in this art, at the time of invention was made to have modified <u>Kim</u>, <u>Clark</u> and "<u>Digital Visual Interface (DVI)</u>, <u>Revision 1.0</u>" by the teaching of <u>Bauch</u>, because including the formation of communication with the primary link having the associated pri_clk and the secondary

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link having the sec_clk would enable a dual single link mode of operation for DVI application, allowing the primary link and the secondary link operate independent of one another.

- 15. As per claim 6, Kim, Clark, "Digital Visual Interface (DVI), Revision 1.0" and Bauch teach the system method and computer program product having connecting the video source and the video display comprise wherein the input stream is pixel data provided at a CLK frequency, wherein the pixel data is transmitted at the T.M.D.S frequency reference that is different 0than the CLK frequency ("Digital Visual Interface (DVI), Revision 1.0", Figure 3-1, page 24, where "input stream" is read on "source video data", "CLK" is read on "native clock rate" and "T.M.D.S frequency reference " is read on "link data rate").
- 16. Claims 12-13 and 19-20 repeat the limitations of claims 5-6 and are therefore rejected accordingly.
- 17. Claims 7 and 14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kim (US Patent: 6,577,303), Clark (US Patent: 5,949,437), the "Digital Visual Interface (DVI), Revision 1.0", and Bauch et al. (US Pub.: US 2003/0152160) as applied to claims 6 and 13 above, and further in view of Hulvey (US Patent 5,940,137).

18. As per claim 7, <u>Kim</u>, <u>Clark</u>, "<u>Digital Visual Interface (DVI)</u>, <u>Revision 1.0</u>" and <u>Bauch</u> teach the system method and computer program product having connecting the video source and the video display comprise wherein the primary link is encoded by converting the 8-bit format to the 10-bit format ("<u>Digital Visual Interface (DVI)</u>, <u>Revision 1.0</u>", Figure 3-1, section 3.1.1 and section 3.1.4 on pages 24-25, where "converting the 8-bit format to the 10-bit format" is read on "8B/10B").

Kim, Clark, "Digital Visual Interface (DVI), Revision 1.0" and Bauch do not teach the system method and computer program product having connecting the video source and the video display further comprises wherein the secondary link is encoded using Manchester II encoding.

Hulvey teaches the transmission of video signal using Manchester encoding (column 2, lines 22-34 and column 5, lines 10-42, where "Manchester" is read on "Manchester II").

Therefore, it would have been obvious to one of ordinary skill in this art, at the time of invention was made to modified <u>Kim</u>, <u>Clark</u>, "<u>Digital Visual Interface (DVI)</u>, <u>Revision 1.0</u>" and <u>Bauch</u> to include for communication the secondary link data using Manchester II encoding.

It would have been obvious to one of ordinary skill in this art, at the time of invention was made to have modified Kim, Clark, "Digital Visual Interface (DVI), Revision 1.0" and Bauch by the teaching of Hulvey, because including communication the secondary link data using Manchester II encoding would enable more effective data transitions and more accurate clock recovery at the receiver.

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19. Claim 14 repeats the limitations of claim 7 and is therefore rejected accordingly.

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chun-Kuan (Mike) Lee whose telephone number is (571)272-0671. The examiner can normally be reached on 8AM to 5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kim N. Huynh can be reached on (571)272-4147. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

C.K.L. 01/30/2006

SUPERVISORY PATENT EXAMINER

2/2/06